

SS3150 THRU SS3200

VOLTAGE RANGE 150 to 200 Volts
CURRENT 3.0 Ampere

FEATURES

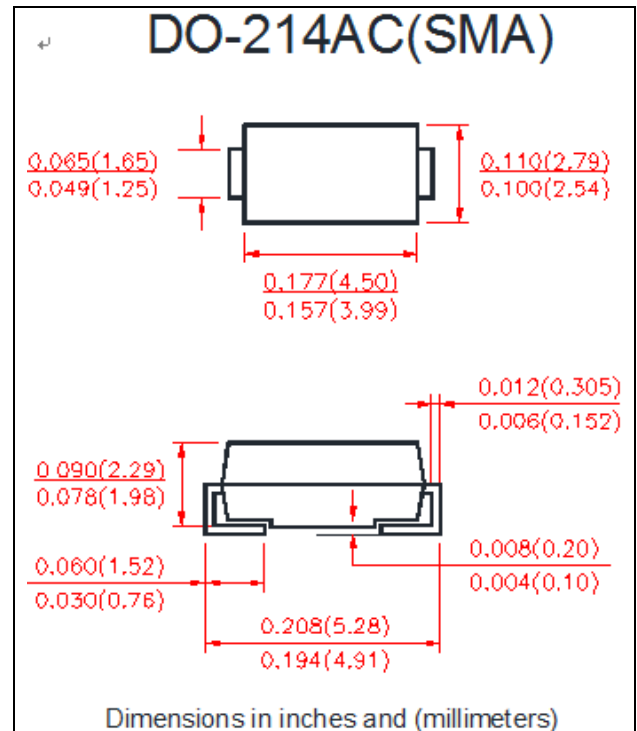
- Low profile surface mount package
- Built in strain relief
- High switching speed
- Low voltage drop, high efficiency
- For use in low voltage high frequency inverters, Free willing ,and polarity protection applications
- Guardring for over voltage protection

MECHANICAL DATA

- Case: Transfer molded plastic
- Epoxy: UL 94V-0 rate flame retardant
- Lead :Solder plated, solderable per MIL-STD-750 method 2026
- Polarity: Color band denotes cathode end
- Weight: 0.002 ounce, 0.064 gram

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

- Ratings at 25°C ambient temperature unless otherwise specified.
- Single phase, half wave, 60Hz, resistive or inductive load.
- For capacitive load derate current by 20%.



	SYMBOLS	SS3150	SS3200	UNIT
Maximum Repetitive Peak Reverse Voltage	V_{RRM}	150	200	Volts
Maximum RMS Voltage	V_{RMS}	105	140	Volts
Maximum DC Blocking Voltage	V_{DC}	150	200	Volts
Maximum Average Forward Rectified Current at T_L see figure 1 $T_L=105^\circ\text{C}$	$I_{(AV)}$	3		Amps
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC method)	I_{FSM}	80		Amps
Maximum Instantaneous Forward Voltage @ 3.0A(Note1)	V_F	0.85		Volts
Maximum DC Reverse Current at rated DC Blocking Voltage per element	I_R	$T_A = 25^\circ\text{C}$	0.5	mA
		$T_A = 100^\circ\text{C}$	10	
Typical Thermal Resistance (Note 2)	$R_{\theta JA}$	55		$^\circ\text{C}/\text{W}$
	$R_{\theta JL}$	12		
Operating Junction Temperature	T_J	150		$^\circ\text{C}$
Storage Temperature Range	T_{STG}	(-55 to +150)		$^\circ\text{C}$

Notes:

1. Pulse test: 300 μs pulse width, 1% duty cycle
2. PCB mounted with 0.2" \times 0.2" (5.0cm \times 5.0cm) copper pads



WEET Technology Company Limited

Schottky Barrier Rectifiers

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FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

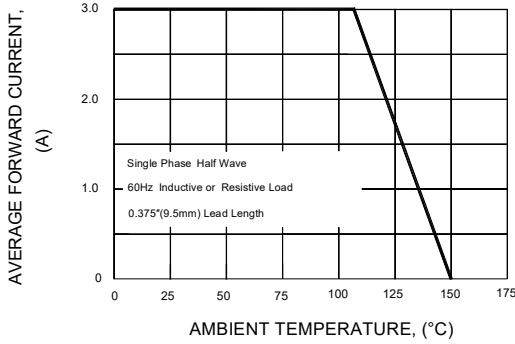


FIG.2-MAXIMUM NON-REPETITIVE PEAK FORWARD SURGE CURRENT

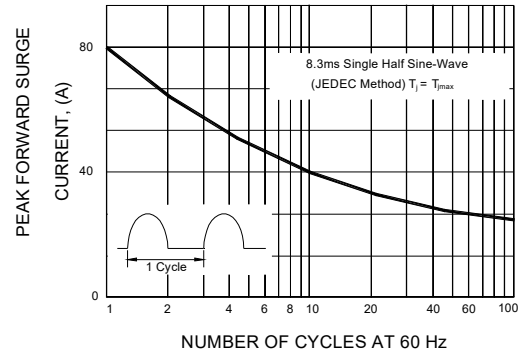


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

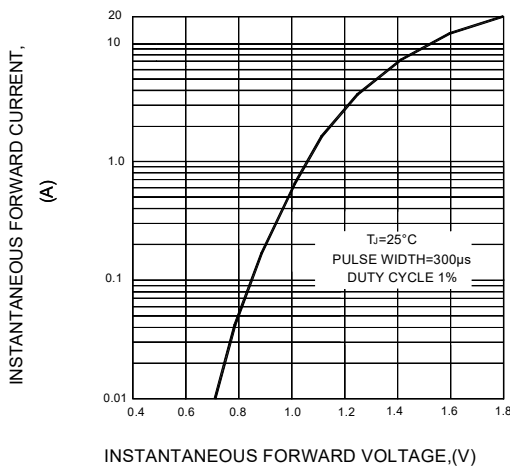


FIG.4-TYPICAL REVERSE CHARACTERISTICS

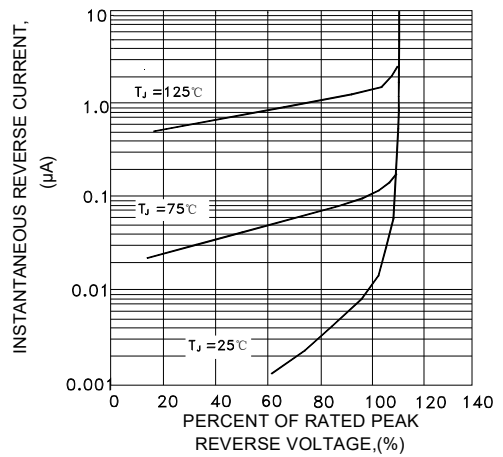
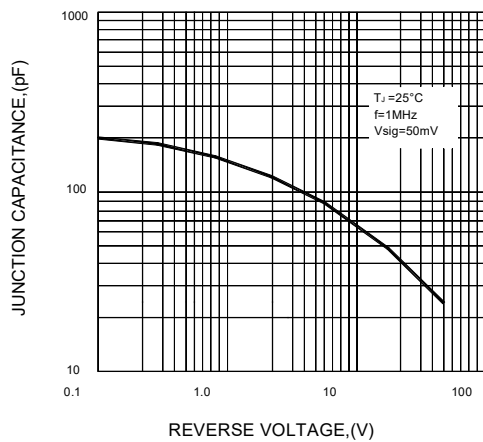


FIG.5-TYPICAL JUNCTION CAPACITANCE



Note: Specifications are subject to change without notice. For more detail and update, please visit our website.